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Patent 7,042,043

PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,042,043

Docket No: 1303.020US1

Issue Date: May 9, 2006

Patentee: Leonard Forbes et al.

Customer No.: 21186

Confirmation No.: 9320

Title PROGRAMMABLE ARRAY LOGIC OR MEMORY DEVICES WITH
ASYMMETRICAL TUNNEL BARRIERSREQUEST FOR CERTIFICATE OF CORRECTIONCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: CERTIFICATE OF CORRECTION BRANCH

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Pursuant to 1.20(a), enclosed please find a check in the amount of \$100.00.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted

LEONARD FORBES ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
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Date:

21 August '06

By:

Timothy B. Chase

Reg. No: 40,957

TBC:sj

08/23/2006 TBESHAN1 00000005 7042043

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Certificate

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of Correction

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 23 day of August 2006

KATHLEEN E. ROTHEBER
Name

Signature

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Ho, Tu-Tu

Patent No.: 7,042,043

Group Art Unit: 2818

Issue Date: May 9, 2006

Docket No: 1303.020US1

Title: PROGRAMMABLE ARRAY LOGIC OR MEMORY DEVICES WITH ASYMMETRICAL TUNNEL
BARRIERS

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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We are transmitting herewith the attached:

☒ A check in the amount of \$100.00 to cover the Certificate of Correction fee.

☒ Request for Certificate of Correction.

☒ Certificate of Correction Form - PTO-1050 (in duplicate)

☒ A return postcard.

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer No: 21186

By: 

Name: Timothy B. Clise

Reg. No. 40,957

TBC:CMG:sj

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KATHLEEN E. ROTHROCK

Name



Signature

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,042,043

Page (1) of 3

DATED : May 9, 2006

INVENTOR(S) : Forbes et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "Other Publications", in column 2, line 5, delete "SiO₂," and insert - - SiO₂, - -, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 5, after "SiO₂," delete "and".

On the face page, in field (56), under "Other Publications", in column 2, line 5, delete "Al₂O₃," and insert - - Al₂O₃, - -, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 5, delete "ZrO₃" and insert - - ZrO₃ - -, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 6, delete "ZrO₂" and insert - - ZrO₂ - -, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 8, delete "Al₂O₃" and insert - - Al₂O₃ - -, therefor.

In column 1, line 12, delete "Baffler" and insert - - Barrier - -, therefor.

In column 1, line 21, delete "Baffler" and insert - - Barrier - -, therefor.

In column 2, line 57, after "DEVICES"" delete "." and insert - - application serial no. 09/945,514. - -, therefor.

In column 21, lines 39-40, delete "Films," application Serial No.____)." and insert - - Films,)." - -, therefor.

In column 25, line 11, in Claim 1, after "gate;" delete "and".

MAILING ADDRESS OF SENDER:

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 25, line 15, in Claim 1, delete "insulator." and insert - - insulator; and the asymmetrical low tunnel barrier integrate insulator having lower barrier height for an erase operation than for a retention operation. - -, therefor.

In column 25, line 42, in Claim 8, after "with the" insert - - asymmetrical - -.

In column 25, line 62, in Claim 10, delete "formed" and insert - - formed by an electric field - -, therefor.

In column 26, line 11, in Claim 13, after "with the" insert - - asymmetrical - -.

In column 26, line 14, in Claim 14, delete "vertical" and insert - - vertical, depletion mode - -, therefor.

In column 26, line 14, in Claim 14, delete "claim 8," and insert - - claim 10, - -, therefor.

In column 26, line 17, in Claim 15, delete "vertical" and insert - - vertical, depletion mode - -, therefor.

In column 26, line 17, in Claim 15, delete "claim 12," and insert - - claim 14, - -, therefor.

In column 26, line 20, in Claim 16, delete "vertical" and insert - - vertical, depletion mode - -, therefor.

In column 26, line 20, in Claim 16, delete "claim 8," and insert - - claim 10, - -, therefor.

In column 26, line 23, in Claim 17, delete "vertical" and insert - - vertical, depletion mode - -, therefor.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 26, line 23, in Claim 17, delete "claim 14," and insert -- claim 16, --, therefor.

In column 26, line 63, in Claim 23, delete "claim 16," and insert -- claim 18, --, therefor.

In column 26, line 64, in Claim 23, after "gate" delete "transistor".

In column 26, line 65, in Claim 23, delete "transistor".

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